



**IEEE Computer Society, Madras Chapter
IEEE Professional Communication Society, Madras Chapter
IEEE Technology Management Council, Madras Chapter
Computer Society of India, Chennai Chapter**

Cordially invite you to a presentation

On

“Vedic Mathematics in Engineering Applications”

by

Mr. V. J. K. Kishore Sonti

Asst. Professor, Sathyabama University, Chennai

on Monday, 10th Mar 2014 at 6.00 p.m.

at CSI Education Directorate, Taramani, Chennai – 600113

(Situated in the opposite lane to Indira Nagar MRTS Railway Station.
About 100 meters away from the Dharmambal Women's Polytechnic &
Opposite to the Institute of Mathematical Sciences and Institute of Hotel Management)

Mr. H.R. Mohan
Chairman
IEEE CS & PCS Madras

Mr. P. Kumar
Chairman
CSI, Chennai

Mr. K.V. Rupchand
Chairman
IEEE TMC

Programme: 6.00: High Tea & Fellowship :: 6.30: Presentation :: 7.45: Dinner (Fruit & Bread)

About the presentation: Vedic Mathematics is the name given to ancient system of mathematics which was rediscovered from Vedas by Sri Bharati Krishna Tirthaji. It is elaborated to sixteen sutras and thirteen sub-sutras. This helps in solving mathematical calculations faster, reduces computational complexity. This is in practice at school level in some of the institutions and also taken as a campaign by some of the service groups in India. In this talk, the importance of this knowledge for engineering applications in particular to circuit branches and information processing will be presented.

About the speaker:



Mr. V.J.K. Kishore Sonti is a gold medalist in bachelor's degree from D.N.R College, Andhra University. He received his Master's Degree in VLSI Design from Sathyabama University. He is waiting to receive his PhD in High Speed Devices from Sathyabama. Currently he works as Assistant Professor in the Dept. of ECE in Sathyabama University. He has to his credit 55 research publications published in international & national Journals and in several conferences at national and international level. He has served as technical chair at various national conferences in and around Chennai. He has delivered around 40 invited talks in workshops and seminars in various engineering colleges and universities in Southern India. His research interests include VLSI Design, Micro Electronics, Mixed Signal Circuits, Material Physics and Nanotechnology. He also offers a certificate course in “Vedic Mathematics and its Engineering Applications” for students of Sathyabama University. His social interest includes teaching economically backward students, who are pursuing their education through foundations like AGARAM. He is a life member of Instrument Society of India and Indian Society of Non-Destructive Testing (ISNT), Chennai Chapter.

To facilitate dinner logistics, pl. pre-register at <http://goo.gl/GTzKg>